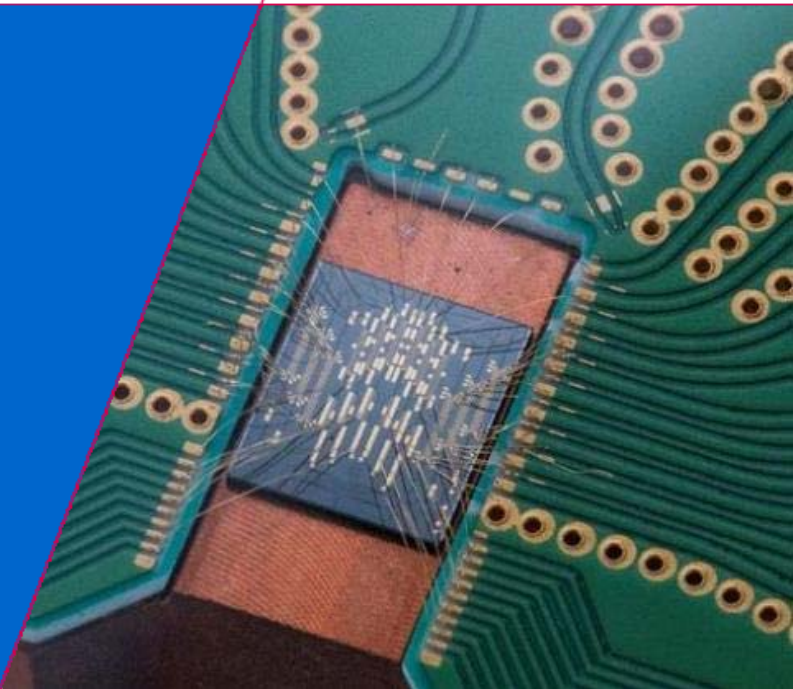


OpenPICs WP3

19th December 2016



TU / **e**

Technische Universiteit
Eindhoven
University of Technology

Where innovation starts

Agenda

WP 3.1 BB Design

- SSC adaptation from WIPE project
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- Work on technology specifications for RF line in January
- DBR + DFB laser: specifications for technology needed

WP 3.3 BB Characterization

- Most up-to-date data on existing FoM measurements.
- Structures for SP19

WP 3.5 Demonstrator

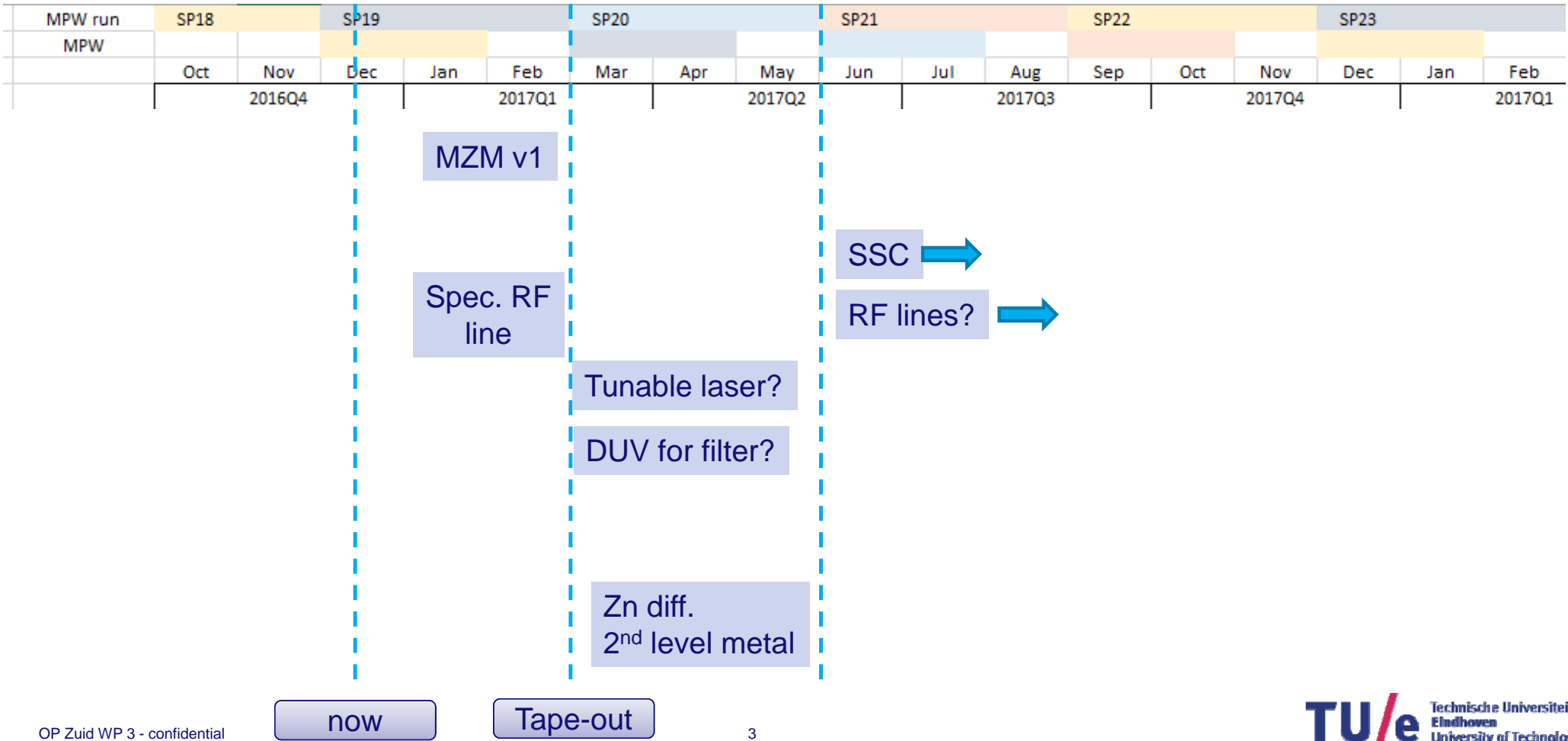
WP 3.2 PDK Content

- what belongs to PDK.

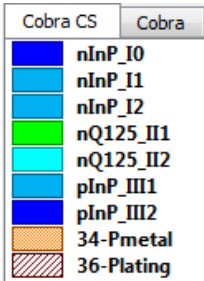
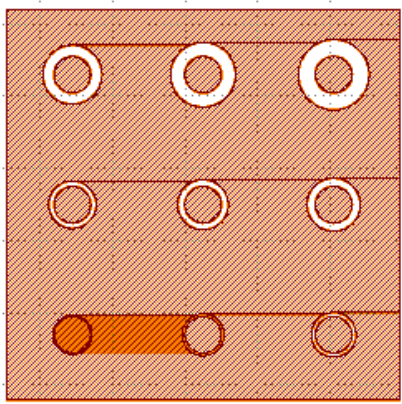
WP 3.4 Design Environment

- Design flow model/data model?
- Contact person from Phoenix
- Entering new BB into library

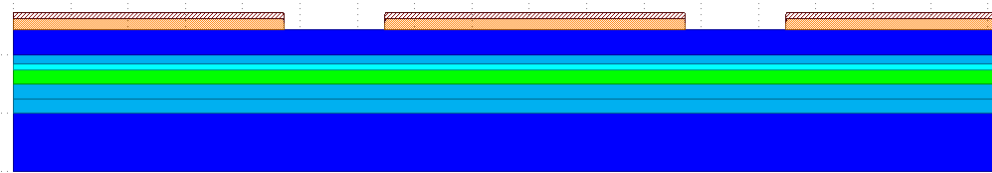
WP 3.1 BB Design



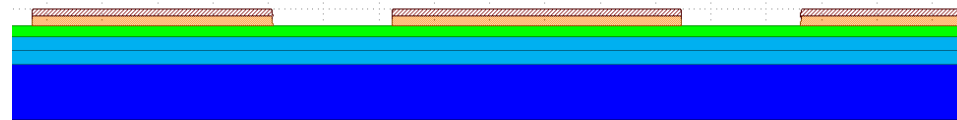
WP 3.3 BB Characterization – cTLM resistance measurement



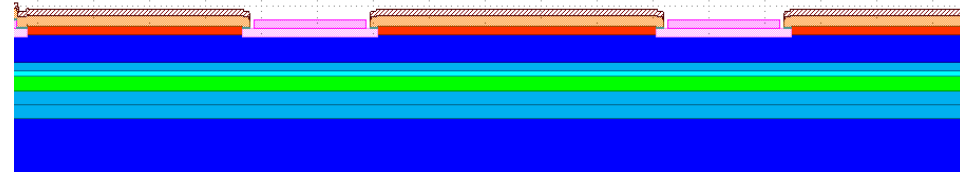
On p-InP



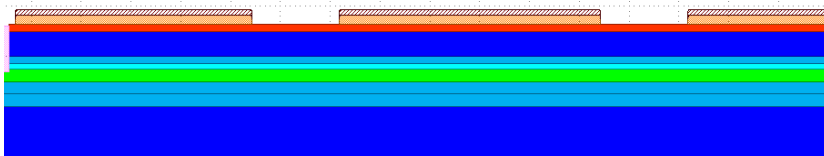
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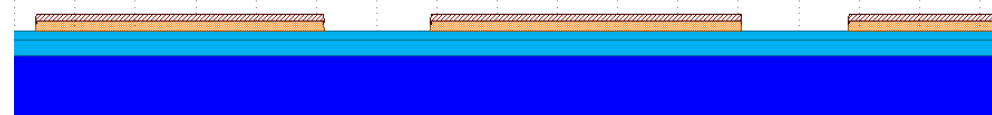
On InGaAs--pInP



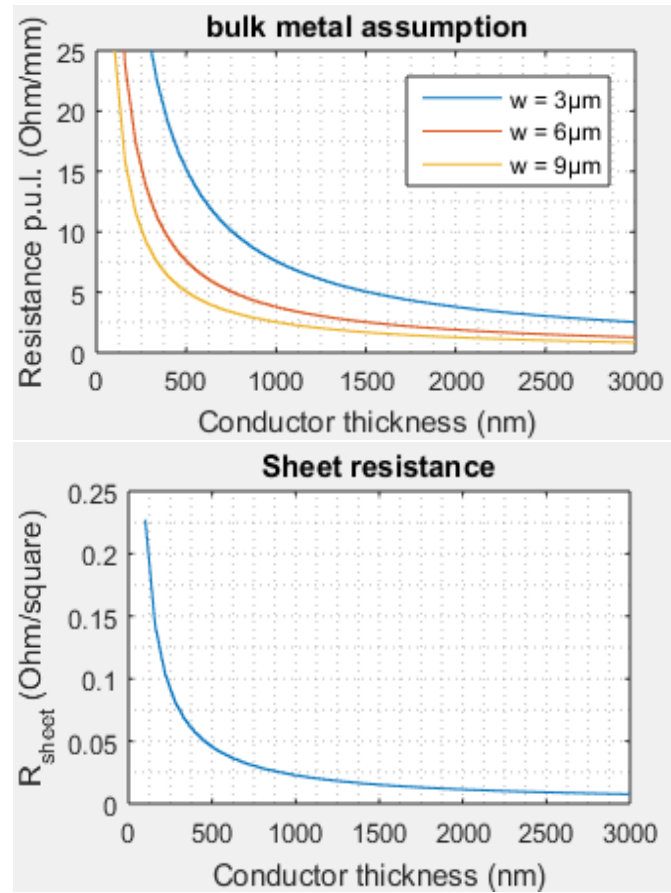
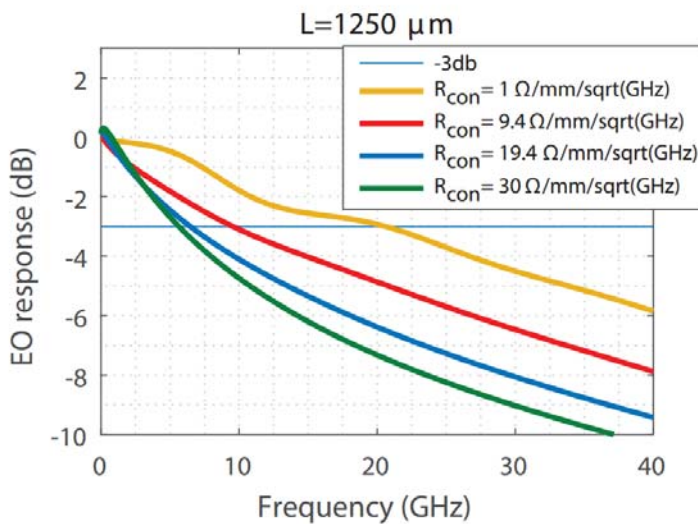
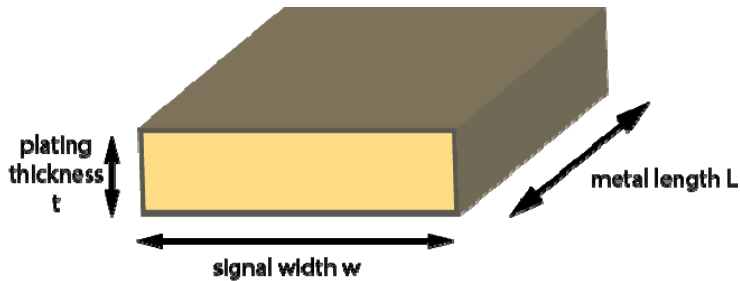
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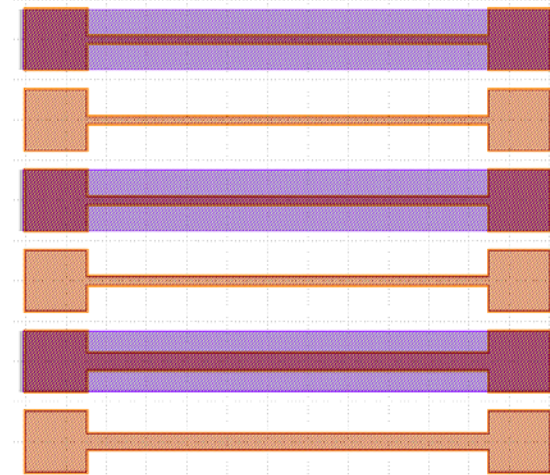
On n-InP



WP 3.3 BB Characterization – DC resistance for plating

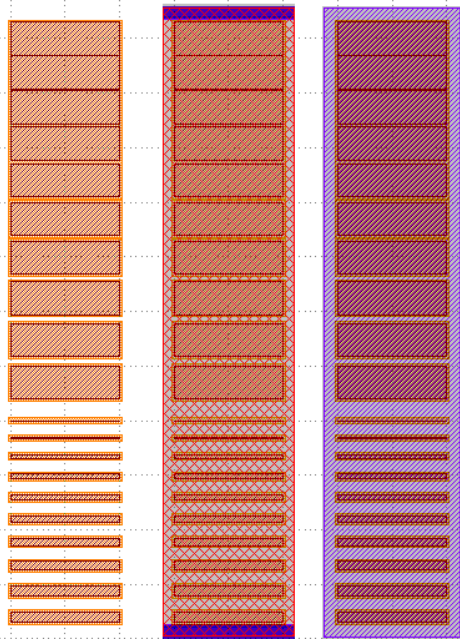


DC resistance measure

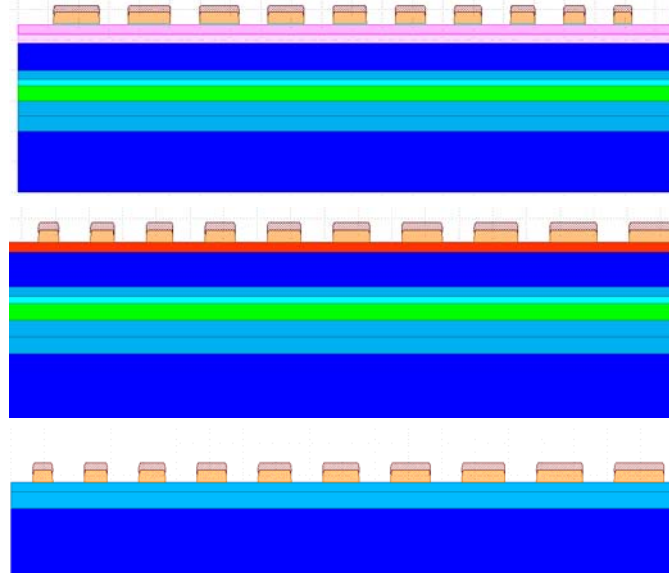


WP 3.3 BB Characterization – metal, recess resolutions

Metal definition

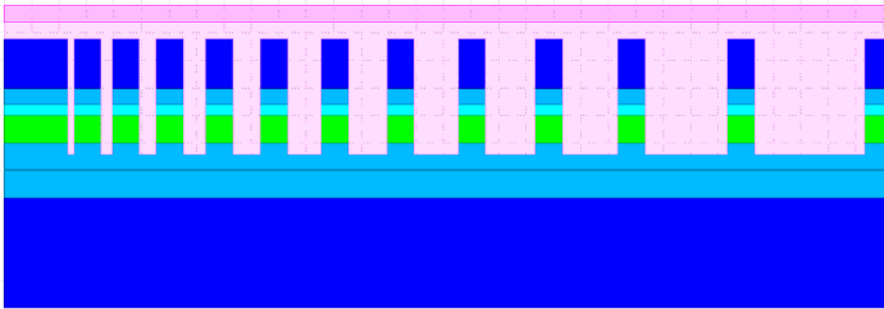


1 μm – 2 μm --... 12 μm

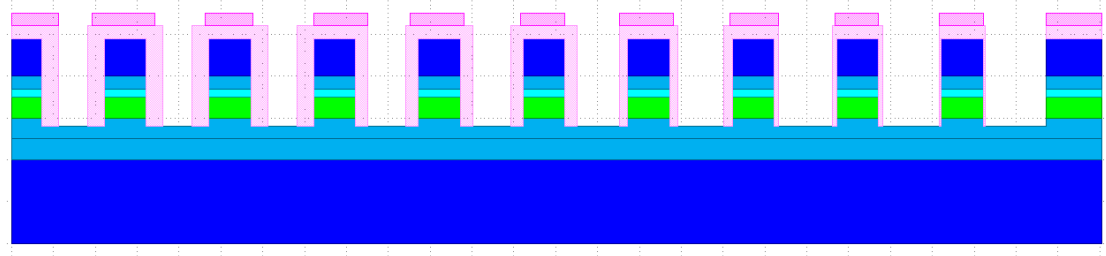


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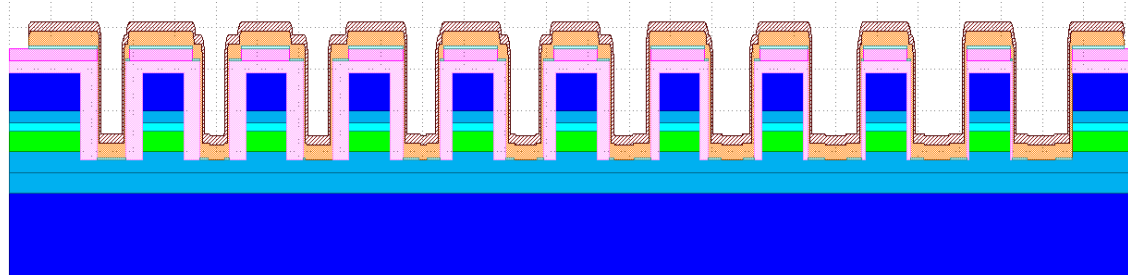
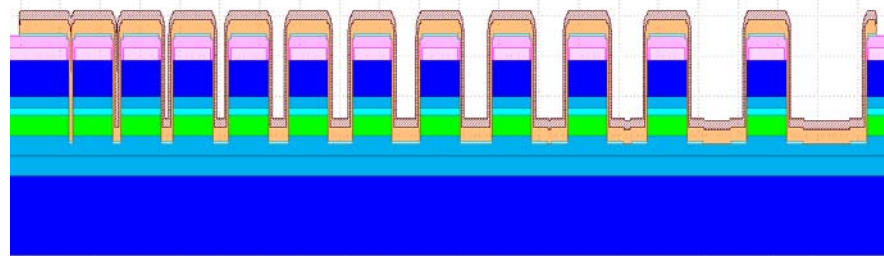
Deep and shallow trenches



Varying apertures for polyopen + plating test



Plating through trenches



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