

Minutes from OpenPICs WP 3 meeting 23-01-2017

Part I (14:00-15:00): Weiming (chairing), Longfei, Saeed, Ronald, Rui

Agenda points	Responsible	Updates?
WP 3.1 BB Design	TU/e, Bright	Presenting time planning
WP 3.2 PDK Content	Smart, TU/e	List of FoMs
WP 3.3 BB Characterization	Smart, TU/e	No updates
WP 3.5 Demonstrator	Effect, Technobis	No updates

Discussion/action points

Nr.	Description	Responsible
1.	Modulator Development Design and simulation should be ready by end February for inclusion into SP 20 (Mach 15). Action required to resolve status of Effect MQW stack. Practical questions such as which PDK to use, what MQW stack information is there, can it be included in active-passive platform or dedicated wafer, access to Effect design documentation need to be answered.	Weiming, Luc, Saeed/ Boudewijn
2.	MPW Schedule and its features Schedule of MPW runs and the planned features per run is needed. Information on MPW cell area assignment for SP20 in openPICs is requested.	Roel, Rui
3.	RF lines TU/e works on 2 nd level passivation (BCB) for RF interconnect plane in WP4. Smart works on 1 st level planarization/passivation. Both will converge to two level interconnect scheme. RF line specifications are needed by Smart.	Weiming
4.	Tunable Laser Development Grating structures are designed for SP19 and in fab. Targeted first design of tunable laser for SP20, but could be subject to postponing.	Ronald
5.	Al-MQW Development TU/e grows Al containing MQW for testing as active material. Smart and TU/e check if existing broad area laser masks can be used.	Rui, Longfei
6.	BB Test Cell BB test cell revision for SP20. Smart checks which Figure of Merits have been already measured and compiles a list with those values. Test structures are fixed with corresponding test procedure.	Erik/Rui, Weiming

Part II (15:00-15:30): Weiming (chairing), Saeed, Ronald, Rui, Xaveer, Marcel, Remco

Agenda points	Responsible	Updates?
WP 3.4 Design Environment	Phoenix, Bright, TUE	No updates

Discussion/action points

Nr.	Description	Responsible
1.	<p>Activities of Phoenix in openPICs</p> <p>We identified two main activities that Phoenix would like to pursue in openPICs:</p> <ul style="list-style-type: none"> -Creation of environment for easy capturing/entry of new building blocks into the design environment, that is into the mask and simulation model libraries and into the PDAflow -Improving of existing design workflow from both user's and foundries' perspectives. 	Marcel, Remco
2.	<p>Reading spt files by Bright</p> <p>Ronald requests reading of spt files by external software in order to facilitate communication between different programs and integration of design flows.</p>	Marcel, Remco, Ronald
3.	<p>Documentation of design workflow for Smart, HHI</p> <p>Formal documentation of the workflow as of now for designing for Smart and HHI foundries is needed, covering from mask generation to BB replacement and final wafer assembly. This documentation can act as basis to improve on existing problems. Phoenix will start and circulate drafts to other partners to complete.</p>	Marcel, Remco, others
4.	<p>Milestones Planning</p> <p>Based on list of existing milestones in WP3.4 Phoenix will draft a more detailed planning involving subtasks and corresponding time frames. That will be first internally discussed in Phoenix and circulated to project partners afterwards.</p>	Marcel, Remco
5.	<p>Phoenix personel</p> <p>Phoenix will discuss possibility of having a person physically at TU/e for a few days a week to support the activities in openPICs</p>	Marcel, Remco

Next meeting: Part I 14:00-15:00, Part II 15:00-15:30, 13-02-2017, Flux 10.177