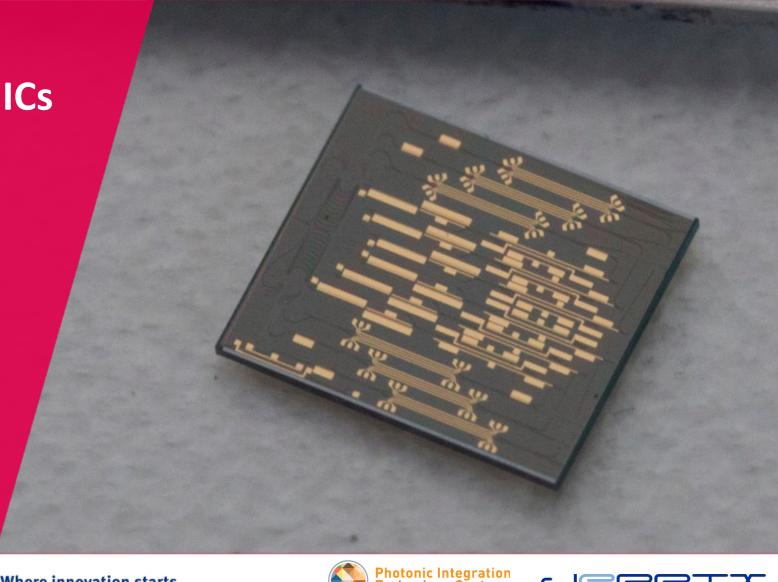
Technische Universiteit **Eindhoven** University of Technology

Open Innovation Photonic ICs OpenPICs

Progress Meeting 04/04/2018





Agenda



14:00	- 14:10	Getting	ready	/
± 1.00	T 1. T 0	Cetting	ready	

14:10 - 14:25 Introduction & Project Timeline

14:25 - 14:40 Smart Photonics Progress

14:40 - 14:55 Bright Photonics Progress

14:55 - 15:10 Phoenix Progress

15:10 - 15:20 Effect Photonics Progress

15:20 - 15:25 Technobis Input

15:25 - 15:40 TU/e Progress

15:40 - 15:50 Demonstrators/Milestones

15:50 - 16:00 Actions/AOB

(Weiming)

(Rui, WP2, WP3, WP4)

(Ronald, WP3)

(Marcel, WP3)

(Saeed, WP3)

(Vincent, WP1, WP3)

(Weiming, Victor, WP1, WP3, WP4)



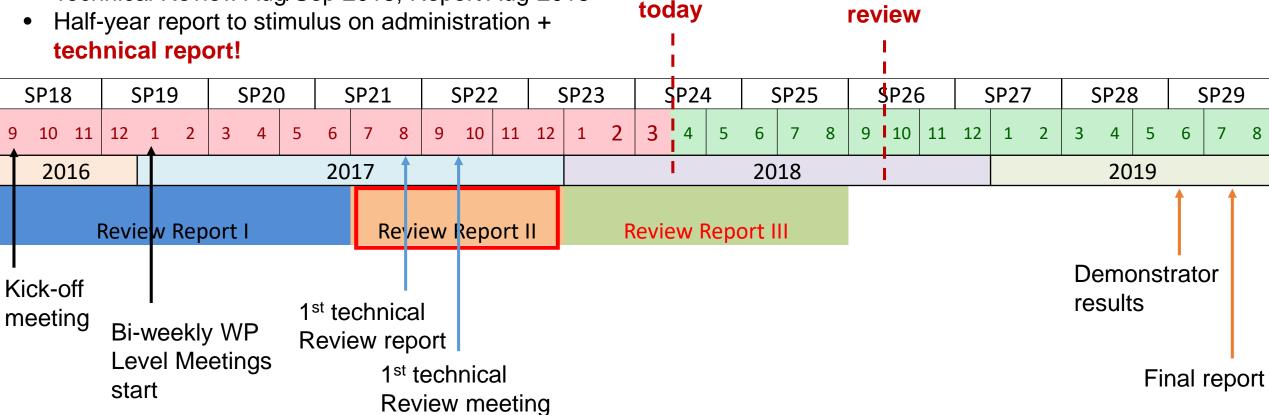


Project Timeline



With 2018

- Monthly progress meeting with ALL partners
- Technical Review Aug/Sep 2018, Report Aug-2018



Technical

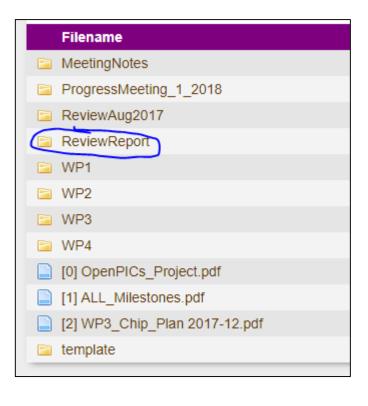


Technical Reports







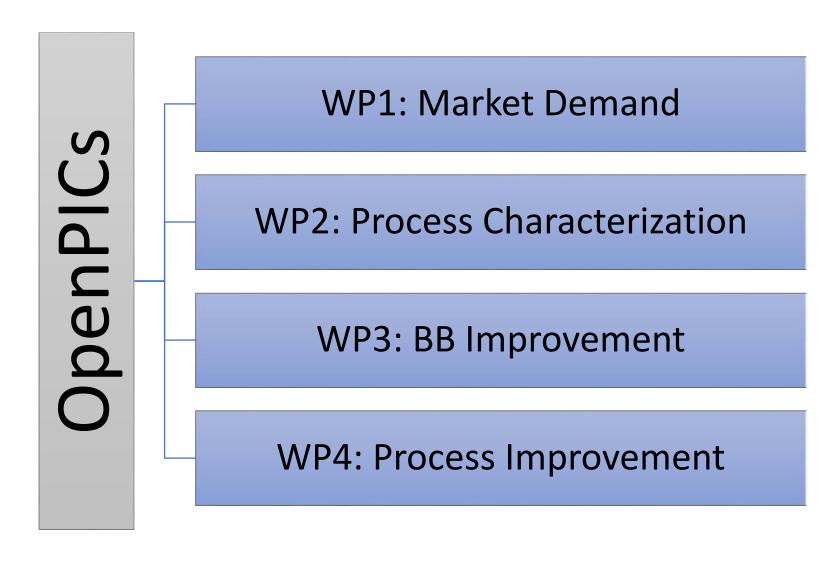






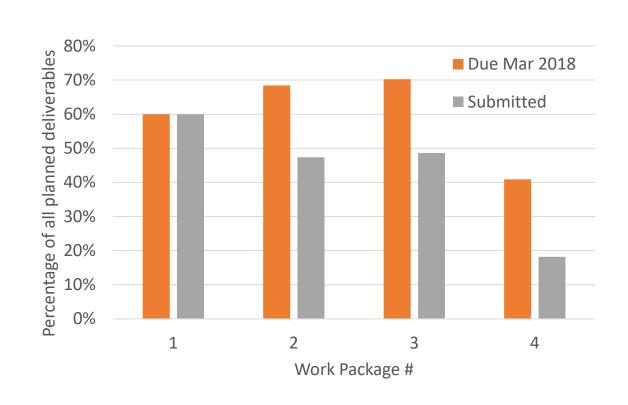
Work Packages

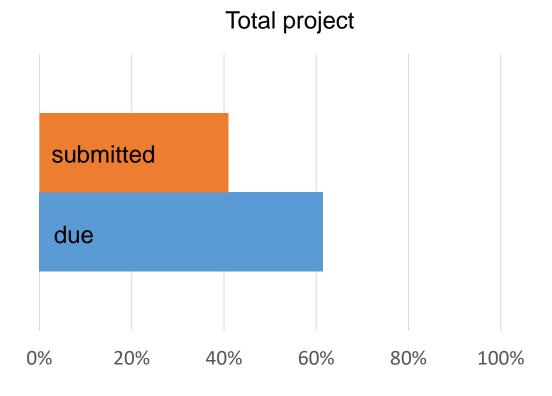




Project Progress









Dissemination





This project receives funding from the OPZuid 2014-2020 Stimulus program of the European Union.

The South Netherlands region is a leader in the field of photonics and is home to leading research[Tacilities and an innovative photonics industry. With the OpenPICs project, the partners develop a strong industrial platform for PIC production and develop the open access tools to make the technology available to a broad range of companies. At present, the following technological challenges need to be overcome to make open access PIC technology suitable for a wide uptake.

- Design: Development of efficient sub-circuits (which are built up from a variety of basic building blocks) in order to reach a "first time right" chip design.
- PDK: Improvement of the PDK which provides a designer with all the information about the measured and expected key figures of merit for each building block, enhancement of the software infrastructure for the PDK and acquisition of the necessary process data to fill the PDK.
- Production: Achieve higher level for the generic photonic integration
 process by means of the development of new building blocks, and the
 optimization and ultimately industrialization of the process of making
 photonic devices on a generic platform.
- Application: The progress in the above three factors determines the
 extent to which application developers are able to obtain quickly,
 reliably and cost-effectively the required functionality from chips which
 can also be produced in large volumes.

Background on Photonic Integrated Circuits:

- Integrated Photonics on Wikipedia
- Technology and application roadmap on Integrated Photonics
- MEMPHIS Project
- Integrated Photonics Ecosystem PhotonDelta

Support in developing your Photonic Integrated Circuits:

- 1ePPIX
- PICS4All
- ACTPHAST

Partner Area

OPZuid documents - partner area

Filename	Type ▼	Size
MeetingNotes	<directory></directory>	<directory></directory>
ReviewAug2017	<directory></directory>	<directory></directory>
ReviewReport	<directory></directory>	<directory></directory>
□ WP1	<directory></directory>	<directory></directory>
□ WP2	<directory></directory>	<directory></directory>
□ WP3	<directory></directory>	<directory></directory>
□ WP4	<directory></directory>	<directory></directory>
[1] ALL_Milestones.pdf	PDF Document	90.5 KB
[2] WP3_Chip_Plan 2017-12.pdf	PDF Document	122.8 KB
OpenPICs_Project.pdf	PDF Document	1.5 MB

Project website

Promote on events, presentations

Promote on partner websites

 Communicate on which events it was mentioned





Last Meeting



Minutes from OpenPICs meeting 15-01-2018

Present: Weiming, Kevin, Victor, Vincent, Rob, Rui, Ronald, Saeed, Marcel

Time: 13:00-15:00

Discussion/action points

Effect Photonics Demonstration Discussion

- two options best effort vs building block freeze
- -400G = 4x (100 Gb/s per channel)
- bandwidth requirements 40-50 GHz
- PAM 4 modulation
- before demonstrator design: Laser-Mod integration approach needs to be tested
- SP27 is last run
- SP24 is try-out run



Last Meeting



Demo Technobis

- SP26/27 targeted
- Process tolerances are important
- spot-size converter performance was requested from Smart
- linewidth of lasers is important
- (action) Ronald connects with Technobis on this.

Spot Size Converter

- more data is needed from Smart
- converter couples too much into the cladding
- (action) Rui supplies more data

Phoenix

- (action) Marcel works on documentation



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(Ronald, WP3)

(Marcel, WP3)

(Saeed, WP3)

(Vincent, WP1, WP3)

(Weiming, Victor, WP1, WP3, WP4)







Effort Table



Man month

Partner	Person	WP1	WP2	WP3	WP4
Effect	Saeed	1.4	0	22.4	0
Technobis	Vincent	2.9	0	4.9	0
Bright	Ronald, Valentina	1.6	0	62	0
Smart	Rob, Rui, Roel	3.5	61	25	24.3
Phoenix	Marcel	1.6	0	39	0
Lionix	Joost	3.2	0	3.3	11.4
TU/e	Victor, Weiming, Michalis, Ekaterina	44.8	0	75.6	61.2

Partner effort to be reflected also in the reporting/documentation